

PTO/SB/82 (09-03)

Approved for use through 11/30/2005. OMB 0851-0035

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REVOCATION OF POWER OF ATTORNEY WITH NEW POWER OF ATTORNEY AND CHANGE OF CORRESPONDENCE ADDRESS	Application Number	09/925,485
	Filing Date	August 10, 2001
	First Named Inventor	Gary Q. Jin
	Art Unit	2133
	Examiner Name	Dildine Jr. R. Stephen
	Attorney Docket Number	AP1106US

I hereby revoke all previous powers of attorney given in the above-identified application.

☐ A Power of Attorney is submitted herewith.

OR

☒ I hereby appoint the practitioners associated with the Customer Number:

33361

☒ Please change the correspondence address for the above-identified application to:☒ The address associated with
Customer Number:

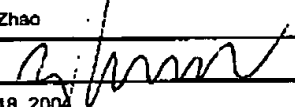
33361

OR

<input type="checkbox"/> Firm or Individual Name					
Address					
Address					
City		State		Zip	
Country					
Telephone		Fax			

I am the:

☐ Applicant/Inventor.☒ Assignee of record of the entire interest. See 37 CFR 3.71.
Statement under 37 CFR 3.73(b) is enclosed. (Form PTO/SB/96)**SIGNATURE of Applicant or Assignee of Record**

Name	Ping-Ya Zhao		
Signature			
Date	October 18, 2004	Telephone	613 271 3788

NOTE: Signatures of all the inventors or assignees of record of the entire interest or their representative(s) are required. Submit multiple forms if more than one signature is required, see below.

☒ *Total of 1 forms are submitted.

This collection of information is required by 37 CFR 1.36. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 3 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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PTO/SB/98 (08-03)

Approved for use through 07/31/2008, OMB 0651-0031

U.S. Patent and Trademark Office, U.S. DEPARTMENT OF COMMERCE

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

STATEMENT UNDER 37 CFR 3.73(b)Applicant/Patent Owner: 1021 Technologies KKApplication No./Patent No.: 09/925,485 Filed/Issue Date: August 10, 1001Entitled: Combination Reed-Solomon and Turbo Coding

1021 Technologies KK, a Corporation
 (Name of Assignee) (Type of Assignee, e.g., corporation, partnership, university, government agency, etc.)

states that it is:

1. ☒ the assignee of the entire right, title, and interest; or
2. ☐ an assignee of less than the entire right, title and interest.
 The extent (by percentage) of its ownership interest is _____ %
 in the patent application/patent identified above by virtue of either:

A. ☐ An assignment from the inventor(s) of the patent application/patent identified above. The assignment was recorded in the United States Patent and Trademark Office at Reel _____, Frame _____, or for which a copy thereof is attached.

OR

B. ☒ A chain of title from the inventor(s), of the patent application/patent identified above, to the current assignee as shown below:

1. From: Gary Q. Jin To: Zarlink Semiconductor Inc.
 The document was recorded in the United States Patent and Trademark Office at
 Reel 012216, Frame 0848, or for which a copy thereof is attached.
2. From: Zarlink Semiconductor Inc To: 1021 Technologies KK
 The document was recorded in the United States Patent and Trademark Office at
 Reel _____, Frame _____, or for which a copy thereof is attached.
3. From: _____ To: _____
 The document was recorded in the United States Patent and Trademark Office at
 Reel _____, Frame _____, or for which a copy thereof is attached.

☐ Additional documents in the chain of title are listed on a supplemental sheet.

☐ Copies of assignments or other documents in the chain of title are attached.
 (NOTE: A separate copy (i.e., the original assignment document or a true copy of the original document) must be submitted to Assignment Division in accordance with 37 CFR Part 3, if the assignment is to be recorded in the records of the USPTO. See MPEP 302.08]

The undersigned (whose title is supplied below) is authorized to act on behalf of the assignee.

October 18, 2004

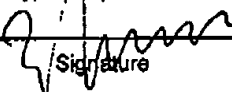
Date

613 271 3789

Telephone number

Ping-Ya Zhao

Typed or printed name



Signature

CTO & Vice President R & D

Title

This collection of information is required by 37 CFR 3.73(b). The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1460.

If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2.

ASSIGNMENT

In consideration of one dollar and other good and valuable consideration, receipt of which is hereby acknowledged, Zarlink Semiconductor Inc. ("Assignor") whose post office address is 400 March Road, Ottawa, Ontario, Canada K2K 3H4 hereby sells, assigns and transfers to 1021 Technologies KK ("Assignee") whose post office address is 77-12 Gurnyoji, Minami-Ku, Yokohama-city, Japan 232-0067 on an "as is" basis all its rights, title and interest to the extent that any such rights, title or interest may exist in and to the following thirteen (13) inventions entitled

Timing Recovery With Minimum Jitter Movement

DMT Bit Allocation with Imperfect TEQ

A Simple Implementation of Turbo Decoder

Efficient Implementation of Large Size FFT

Method And Apparatus for Connecting Broadband Voice And Data Signals to Telephone Systems

Parallel Turbo Trellis-Coded Modulation

Turbo Trellis-Coded Modulation

Combination Reed-Solomon and Turbo Coding

Method and Apparatus for Connecting Broadband Voice and Data Signals to Telephone Systems

DMT Peak Reduction Without Affecting Transmission Signal

Flexible Bit Selection Using Turbo Trellis-Coded Modulation

Fast Fourier Transform Signal Processing**Protection of Subscriber Line Interface Circuits (SLICS) without Degradation in Longitudinal Balance**

which were the subject of certain patent applications and/or issued patents in certain countries as identified and listed in Schedule A attached hereto and forming part hereof together with any rights, title and interest in and to any patents which may be re-issued based upon such patent applications and/or issued patents identified and listed in Schedule A.

And Assignor also agrees to do all such lawful acts and to execute without further consideration such further assurances, assignments, applications and other instruments as may reasonably be required by Assignee at Assignee's expense to vest such patent applications and/or issued patents identified and listed in Schedule A in Assignee.

By this Assignment, Assignee acknowledges that it assumes all responsibility for such patent applications and/or issued patents identified and listed in Schedule A commencing on the date this Assignment is executed by the parties.

SIGNED at Ottawa, Ontario this 4th day of October, 2004.

ZARLINK SEMICONDUCTOR INC.

By: 

Name: Desmond Byrne

Title: Vice President, Legal

STATEMENT OF WITNESS

I, Gail Casselman, whose full post office address is 5 Glenmoriston Ave., Ottawa, Ontario K2L 2S3 was personally present and did see Desmond Byrne who is known to me, execute this Assignment.

By: 

(signature of witness)

SIGNED at Ottawa
this 4th day of October, 2004.

1021 TECHNOLOGIES KK

By: [Signature]Name: Ping-Ya ZhaoPosition: CTO & VP R&D

STATEMENT OF WITNESS

I, WENTAO LI, whose full post office address is 17 Portadown Crescent
Kanata, ON K2W 1E8
was personally present and did see
who is known to me, execute the above Assignment.

By: [Signature]
(signature of witness)

SCHEDULE A

Invention Title	Country	Date Filed	Application Serial No.	Patent No.
Timing Recovery with Minimum Jitter Movement	Great Britain	24-Oct-97		2330736
	France	19-Oct-98	9813231	
	US	30-Sep-98		6,138,244
DMT Bit Allocation with Imperfect TEQ	Great Britain	23-Dec-99		2357837
	US	20-Dec-00	09/739,979	
	China	23-Jun-02	817662.0	
A Simple Implementation of Turbo Decoder	US	26-Feb-01	09/791,808	
	US	31-Aug-01	09/942,628	
Efficient Implementation of Large Size FFT	China	04-Sep-01	1131035.9	
	US	12-Apr-01	09/832,980	
Method and Apparatus for Connecting Broadband Voice and Data Signals to Telephone Systems	Germany	24-Apr-01	101 20 197.4	
	US	01-May-00		6,757,859
Parallel Turbo Trellis-Coded Modulation	China	27-Apr-01	1115658.9	
	France	27-Apr-01	01 05784	
Turbo Trellis-Coded Modulation	US	01-May-00		6,671,327
	France	27-Apr-01	01 05783	
Combination Reed-Solomon and Turbo Coding	Great Britain	10-Aug-00		2366199
	US	10-Aug-01	09/926,485	
	Germany	09-Aug-01	10139116.1	
	France	10-Aug-01	01 10707	
Method and Apparatus for Connecting Broadband Voice and Data Signals to Telephone Systems	Great Britain	26-Jun-00		2364200
	US	21-Jun-01		6,974,845
	Germany	25-Jun-01	101 30 029.9	
	France	28-Jun-01	108389	
DMT Peak Reduction Without Affecting Transmission Signal	US	01-Mar-02	10/085,061	
	France	01-Mar-02	02 02603	
	Japan	01-Mar-02	2002-104528	
Flexible Bit Selection Using Turbo Trellis-Coded Modulation	Great Britain	23-Mar-01	107295.8	
	US	18-Mar-02	10/098,474	
	France	22-Mar-02	02 03602	

←

P.
JB

Oct. 04, 2004

SCHEDULE A

Invention Title	Country	Date Filed	Application Serial No.	Patent No.
Fast Fourier Transform Signal Processing	US	30-Jan-03	10/353,983	
	Europe	30-Jan-03	3100197.7	
Protection of Subscriber Line Interface Circuits (SLICS) without Degradation in Longitudinal Balance	US	28-Jan-01	09/770,610	